

AMENDMENTS TO CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-9. (Canceled)

10. (Currently Amended) ~~A Pixel Circuit For Liquid Crystal Display Using Static Memory~~ pixel circuit for a liquid crystal display using a static memory for lowering power consumption ~~via~~ by combining an ~~analogue~~ analog and a digital circuit, wherein the circuit comprises:

a first multiplexer, having a plurality of current switching elements;

a demultiplexer, having a plurality of current switching elements;

a static memory, connecting to a scanning line connected to the first ~~multiplexer~~ multiplexer and the demultiplexer, for storing the digital voltage signals stored in the demultiplexer;

a thin film transistor, for connecting a scanning line and a data line, acting as a control switch of the circuit;

a capacitor, ~~connecting~~ connected to the thin film transistor, where ~~analogue~~ analog or digital signals from the data line are stored; and

a ~~second~~ switch device, ~~connecting~~ connected to the first multiplexer, the demultiplexer and a liquid crystal unit.

11. (Currently Amended) ~~The Pixel Circuit For Liquid Crystal Display Using Static Memory~~ pixel circuit for a liquid crystal display using a static memory of claim 10, wherein said first multiplexer further comprises a general voltage terminal and a reference voltage terminal.

Serial Number 10/632,911

12. (Currently Amended) ~~The Pixel Circuit For Liquid Crystal Display Using Static Memory~~
pixel circuit for a liquid crystal display using a static memory of claim 10, wherein the ~~second~~
switch device connects to a mode terminal via a second signal line for controlling switch signals.

13. (Currently Amended) ~~The Pixel Circuit For Liquid Crystal Display Using Static Memory~~
pixel circuit for a liquid crystal display using a static memory of claim 10, wherein the
demultiplexer further comprises:

- a selection terminal;
- a demultiplexer input terminal;
- a first mode terminal; and
- a second mode terminal.

14. (Currently Amended) ~~The Pixel Circuit For Liquid Crystal Display Using Static Memory~~
pixel circuit for a liquid crystal display using a static memory of claim 13, wherein said
demultiplexer further connects to a mode terminal.

15. (Currently Amended) ~~The Pixel Circuit For Liquid Crystal Display Using Static Memory~~
pixel circuit for a liquid crystal display using a static memory of claim 13, wherein the input
terminal of the demultiplexer further connects to the thin film transistor.

16. (Currently Amended) ~~The Pixel Circuit For Liquid Crystal Display Using Static Memory~~
pixel circuit for a liquid crystal display using a static memory of claim 13, wherein the output
terminal of the first mode terminal further connects to the ~~second~~ switch device.

17. (Currently Amended) ~~The Pixel Circuit For Liquid Crystal Display Using Static Memory~~
pixel circuit for a liquid crystal display using a static memory of claim 13, wherein the output
terminal of the second mode terminal further connects to the static memory.